

Suspended Tunnel Junction Bolometers For Terahertz Range

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Abstract: Implementation of high resolution passive THz cameras operated at cryogenic temperatures of a few Kelvin benefits from a large number of pixels. At present, building a cryogenic multiplexed read-out circuit represents a challenging task. Here we propose and demonstrate broadband niobium-based tunnel junction bolometers operating in equilibrium regime at 4 K, and read out with a room temperature amplifier with feedback. The implemented scheme allows independent impedance matching for the THz antenna and for the readout. Electrical feedback in the readout scheme eliminates the need for magnetic fields to suppress the supercurrent of the junction. We present electrical and preliminary optical measurements of the detectors. We also analyze a possible scheme for time-domain multiplexing.

Keywords: THz imaging, bolometers, SIS junction.

1. INTRODUCTION

Passive real-time THz imaging based on scanning optics with single detectors or small arrays of hot-spot bolometers have been demonstrated recently [1-3]. Scanning compromises resolution and adds complexity. Therefore “staring” focal plane arrays with static optics are preferable. With increasing number of pixels and limitations set by cryogenics multiplexed readout becomes a necessity. This in turn sets requirements for the detectors. For time-domain multiplexing a bolometer should have a sufficiently low noise equivalent power (NEP) together with sufficient power output to overcome the postamplifier noise. In case of semiconducting readout amplifiers this typically requires relatively large dynamic impedance of the detector element to achieve noise matching. Superconducting tunnel junction (SIS) detectors are well-known THz radiation detectors anticipated to be suitable for large multiplexable arrays [4-7]. Direct photon assisted tunneling based SIS-detectors offer good sensitivity and noise performance, but within a limited bandwidth [4, 5] due to antenna impedance-matching issues. Broadband SIS detectors based on Cooper-pair breaking have been demonstrated recently [6]. They detect both THz photons and THz phonons generated by incident radiation and they function efficiently at sub-Kelvin temperatures. For practical THz scanners simpler cryogenics would be a great benefit [1]. Here we introduce broadband SIS-bolometers operating in an equilibrium mode for a 4 K temperature regime and present proof-of-principle measurements of the first prototype devices. We suggest that they are potentially more practical for multiplexing than the hotspot bolometers, for which two-stage thermal circuits are proposed to

overcome the problem of limited power gain bandwidth [8, 9].

2. SUSPENDED SIS JUNCTION AS A BOLOMETER

Our devices utilize Nb-AlO_x-Nb SIS junction as a thermometer heated by the radiation via a separate suspended resistive element thermally coupled to the suspended junction. Such an arrangement allows for independent optimization of optical coupling matching to the absorber and electrical impedance matching to the read-out amplifier unlike in conventional SIS detectors or hotspot bolometers presently used in 4K THz scanners. The temperature sensitivity dI/dT of an ideal SIS junction can be numerically derived by calculating net quasiparticle current I across the tunnel barrier of resistance R_T with standard single-electron tunneling approach assuming BCS type of density of states (DOS) in a superconductor. A finite DOS within the superconducting gap [10, 11] is commonly used to account for an excess current – a well-known performance-limiting factor for real tunnel junctions. This excess current persists through the sub-gap energy states which originate due to many reasons [12] such as microscopic imperfections both in a tunnel barrier and in a superconductor, high-order tunnelling processes, dissipative high-frequency electromagnetic environment [12], quasiparticle lifetime broadening due to the imaginary part of superconducting energy gap [11, 13], to mention few of those. In our case reasonable junction quality is required to have the excess current lower than the thermally excited quasiparticle current in order to minimize the contribution of the former one to the shot noise $S_I = 2eI \coth(eV/2k_B T)$.

The electrical noise equivalent power NEP with noise contributions from the detector [14] and the readout circuit [15] is then written as

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$$\text{NEP}^2 = \mathcal{R}^{-2} \left[S_I + \frac{2k_B T_{n,a}}{R_d} \left(\frac{R_d}{R_a} + \frac{R_a}{R_d} \right) \right] + 4k_B T^2 G, \quad (1)$$

where $\mathcal{R} = G^{-1} dI / dT$ is the current responsivity of the detector, G is the thermal conductance from the junction to the substrate and $R_d \equiv dV / dI$ is the dynamic resistance of the junction. The noise temperature and the optimum noise matching impedance of the readout amplifier are $T_{n,a} = (S_{V,a} S_{I,a})^{1/2} / 2k_B$ and $R_a = (S_{V,a} / S_{I,a})^{1/2}$, respectively. Here $S_{V,a}^{1/2}$ and $S_{I,a}^{1/2}$ are the input referred equivalent voltage and current noise spectral densities of the amplifier. The first (second) term in the brackets of (1) is the shot noise (amplifier noise) contribution. With small G the bolometer becomes phonon noise limited (the last term of (1)) while the phonon noise contribution itself also decreases as discussed in detail below (Fig. 4). Low resistance junctions are preferable in order to minimize the shot-noise contribution $\sqrt{S_I} / \mathcal{R} \propto \sqrt{R_T}$. Another noticeable fact is that a typical dynamic resistance of a few kOhm for Nb-AlO_x-Nb junctions at 4 K allows the noise matching ($R_d \approx R_a$) of the detector and the preamplifier.

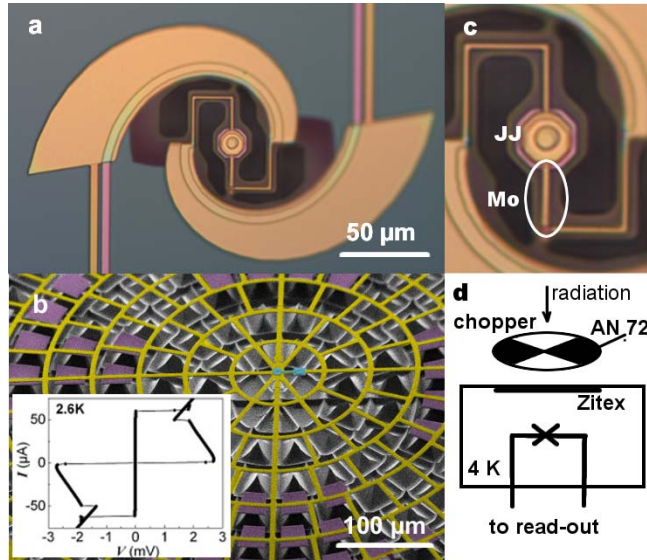


Fig. (1). (a) Logarithmic spiral antenna with suspended tunnel junction bolometer and Mo resistor. (b) Spiderweb Nb/Mo bolometer with a tunnel junction in the center with a IV -curve at 2.6 K in the inset. (c) Zoomed image of the JJ and the resistor. (d) Measurement schematics of optical response of the bolometer.

3. FABRICATION AND EXPERIMENTAL SETUP

The bolometer structures have been fabricated based on Nb-AlO_x-Nb trilayer junction SQUID process with junctions size of 6 μm in diameter [16]. An additional fabrication step of isotropic SF₆ plasma etch was applied to release the structures from the substrate. Molybdenum was used as the resistive material in the radiation absorbing layers. While these devices have been intended for electrical characterization only, we have investigated structures with two types of absorbing elements: a logarithmic-spiral antenna with a suspended stack of a SIS junction coupled to

a heater (Fig. 1a, c) in the spirit of bridge bolometers [17], and a suspended spider-web absorber with a junction in the middle (Fig. 1b). All the suspended parts are supported from below by a silicon oxide layer. The working junctions for the spider web structures were found in a separate cooldown test, for which the IV data at 2.6 K is presented in the inset of Fig. (1b). Below we will present more detailed measurement results on the logarithmic antenna structure.

The log-spiral antenna bolometer was tested in a cryogen-free pulse tube refrigerator. For signal read-out we have used low noise differential-pair FET-based amplifier electronics [18] ($S_{V,a}^{1/2} \approx 0.8 \text{ nV/Hz}^{1/2}$ and $S_{I,a}^{1/2} \lesssim 15 \text{ fA/Hz}^{1/2}$, with these values we find $R_a \gtrsim 53 \text{ k}\Omega$ and $T_{n,a} \lesssim 0.4 \text{ K}$). We applied a readout method including voltage bias and current readout based on active feedback (schematically shown in Fig. 2a) allowing for stable voltage biasing of the junction within the gap without magnetic suppression of the Josephson coupling.

4. MEASUREMENTS

The mixed-down noise component due to Josephson oscillations is estimated to be orders of magnitude smaller than other noise contributions in the operating range of biasing voltages. The tunnel resistance of the junction is measured to be $R_T = 120 \Omega$ with the sub-gap quasiparticle current $I = 1 \mu\text{A}$ at bias voltage $V = 1.5 \text{ mV}$ at 4.4 K (Fig. 2a). From numerically calculated IV curves using ideal BCS-gap model we find quasiparticle current $I = 0.59 \mu\text{A}$ ($eIR_T = 0.05\Delta(0)$ at 4.4 K with $\Delta(0) = 1.4 \text{ meV}$). This indicates that the excess leakage current in the junction is about $0.4 \mu\text{A}$ in contrast to near-BCS limited behavior of non-released junctions fabricated with the same junction process [19]. This may be due to the release-etching step, though its only effect is to enhance the shot noise by about a factor of $\sqrt{2}$ as compared to that of the ideal junction.

From the measured IV s (Fig. 2a) at various bath temperatures we obtain responsivity $dI / dT = 0.57 \mu\text{A/K}$ at $V = 1.4 \text{ meV}$ at 4.4 K, in accordance with its value $0.58 \mu\text{A/K}$ derived from temperature dependent IV s computed with ideal BCS model. Fig. (2a) also shows that, in the subgap region, the responsivity depends only weakly on the temperature and bias voltage. The maximum of the dynamic resistance of $7 \text{ k}\Omega$ (Fig. 2b) is achieved at the optimal bias point $V_{opt} = 1.7 \text{ mV}$, i.e. only about a factor of 4 times smaller than the noise matched case. The measured total current noise I_N referred to the amplifier input (averaged over the readout band 4.5–5.0 kHz) is plotted against biasing voltage in Fig. (2c) at a few bath temperatures. From this figure the contribution of the thermal noise of the feedback resistor $R_{fb} = 30 \text{ k}\Omega$ corresponding to current noise of about $0.7 \text{ pA/Hz}^{1/2}$ has been subtracted. The feedback noise is a practical flaw in our test system avoidable by proper selection of R_{fb} , or by cooling down the resistor. The lowest input current noise $I_N = 0.6 \text{ pA/Hz}^{1/2}$ is

achieved at V_{opt} at 4.4 K. Also, in a wide range of voltage biases 1.2–2.2 mV around V_{opt} the noise is detector-limited and the amplifier noise contribution is negligible. Only in low- R_d operating points the amplifier matching becomes worse and the amplifier voltage noise becomes dominant.

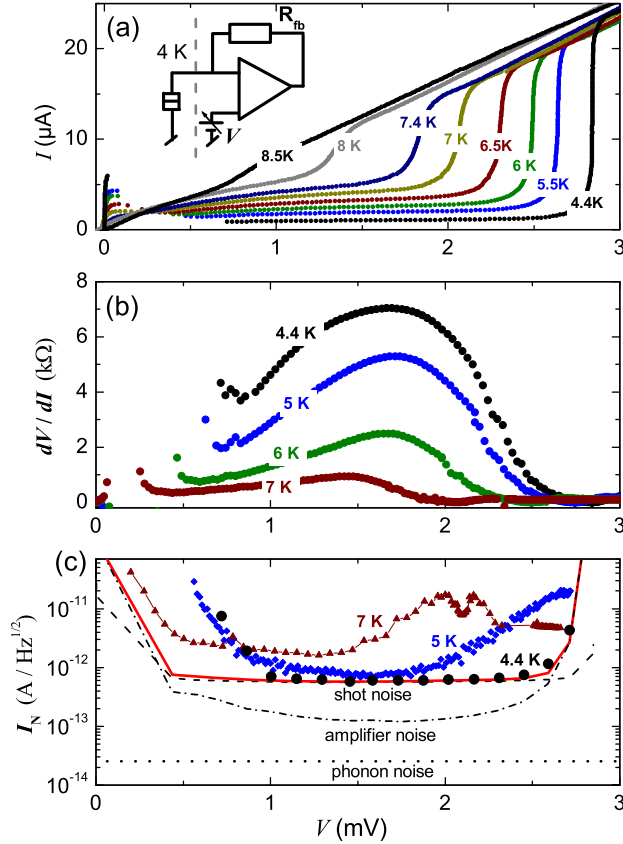


Fig. (2). (a) Measured IV -curves at 4.4..8.6 K. (b) Dynamic resistance derived from the measured IV 's at a few bath temperatures. (c) Averaged input current noise spectral density measured against bias voltage at a few bath temperatures (dots). The lines show current noise contributions calculated for $T = 4.4$ K with Eq. (1) for (I, V) and R_d values taken from measured IV -data, with $dI/dT = 0.57 \mu\text{A}/\text{K}$, $R_d = 53 \text{ k}\Omega$, $T_{na} = 0.4 \text{ K}$, $G = 400 \text{ nW}/\text{K}$ as in the experiment. Red line shows the total input current noise. Dashed lines show the contributions of shot noise (dashed), amplifier noise (dashed-dotted) and the phonon noise (dotted).

To determine G , we have performed thermal conductance measurements on the test structures of suspended junctions with the heater resistors. The heater was driven by a small DC power and the total thermal conductance was inferred by comparing the junction responses to elevated temperature and induced heater power. Based on the geometry we estimated the thermal conductance between the junction and the log-antenna to be $G \approx 400 \text{ nW}/\text{K}$. Assuming $0.1 \text{ W}/(\text{m} \cdot \text{K})$ thermal

conductivity for SiO_2 from the literature [20], about $140 \text{ nW}/\text{K}$ of this comes from the SiO_2 layer including unwanted parasitic thermal links (see Fig. 1c) that remained after the etch-release fabrication step due to the finite $2 \mu\text{m}$ contact-lithography resolution. The rest of $260 \text{ nW}/\text{K}$ originates from an unoptimized thickness of 650 nm of Nb leads. From these data we estimated 4 K thermal conductivity $6.7 \text{ W}/(\text{m} \cdot \text{K})$ of Nb. This value is close to the Wiedemann-Franz thermal conductivity $6.5 \text{ W}/(\text{m} \cdot \text{K})$ with the resistivity $3.6 \times 10^{-8} \Omega\text{m}$ at 10 K , reduced by a factor of 5 with respect to the measured 300 K resistivity. With $G = 400 \text{ nW}/\text{K}$ the electrical $\text{NEP} = I_N / \mathcal{R}$ reads $\approx 4 \times 10^{-13} \text{ W}/\text{Hz}^{1/2}$ (Fig. 4a) and the detector operates in the shot noise limit.

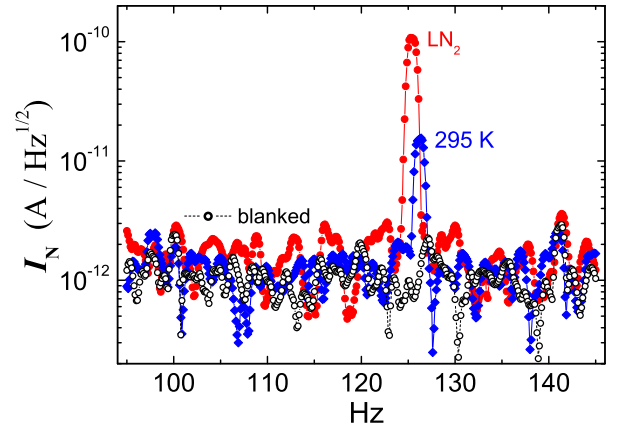


Fig. (3). Optical response of the log-antenna bolometer seen as a spike at the chopping frequency of 125 Hz in the 50 Hz band of the noise spectrum.

We have also carried out preliminary optical response measurements against room temperature and liquid nitrogen temperature backgrounds using a chopper at the frequency of 125 Hz (Fig. 1d). Zitec [21] filters in cryostat windows provided a low pass cutoff at $\sim 1 \text{ THz}$. We observed a pronounced optical signal in the noise spectrum at the corresponding chopping frequency as shown in Fig. (3). The signal from the liquid nitrogen background was about one order of magnitude stronger than the room temperature background. Since our prototype bolometers chips were designed mainly for electrical characterization, we give only the rough estimate for the radiometric noise equivalent temperature difference ($\text{NETD} \sim \Delta T / \text{SNR}$) from the SNR ratios found from the power levels of the optical signals in Fig. (3). $\text{SNR} \sim 130$ for liquid nitrogen backgrounds with $\Delta T \sim 300 - 77 \sim 200 \text{ K}$. Thus $\text{NETD} \approx 1 - 2 \text{ K}$ for 1 s integration time. This is the expected radiometric temperature resolution [22] as for lithographic antenna bolometer structures used with no lens and front-side radiation facing the optical coupling is very poor ($\leq 10^{-3}$). With improved optical coupling the SNR ratios are expected to be of at least 100 times higher to become performance competitive with STJ detectors at 4.2 K reported in ref. [4].

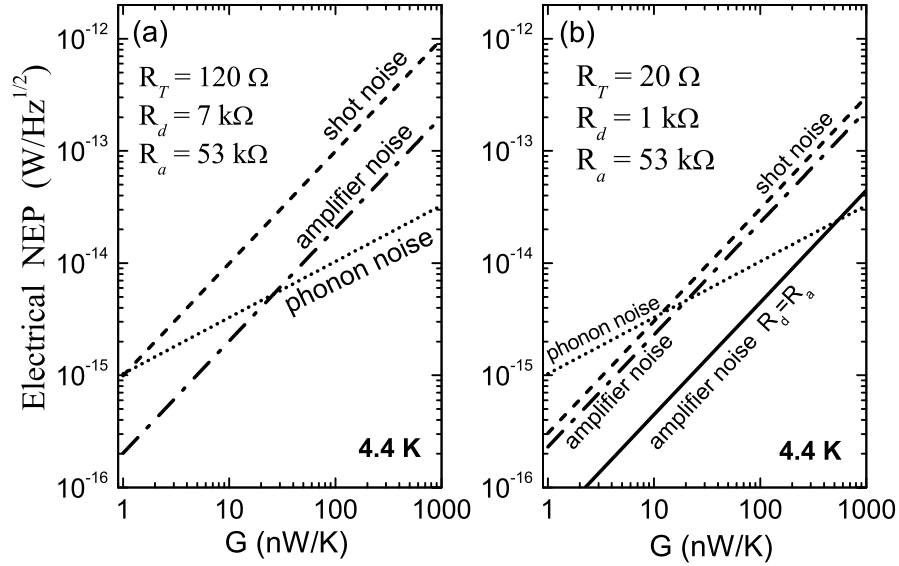


Fig. (4). (a) Calculated (with Eq. (1), $I = 1 \mu\text{A}$, $dI/dT = 0.57 \mu\text{A/K}$) shot noise (dashed), amplifier noise (dashed-dotted) and phonon noise (dotted) contributions to electrical NEP vs G at 4.4 K with the amplifier and junction parameters as in the experiment: $T_{n,a} = 0.4 \text{ K}$, $R_a = 53 \text{ k}\Omega$; $R_d = 7 \text{ k}\Omega$, $R_T = 120 \Omega$. (b) Optimized case for $R_T = 20 \Omega$, $T_{n,a} = 0.4 \text{ K}$ and $R_d = (1/53)R_a$. The solid line shows the noise matched limit $R_d = R_a$.

5 MULTIPLEXING RESOLUTION

5.1. Resolution of an Optimized Single Pixel

With an optimized detector design, a thermal conductance of 10 nW/K at 4 K can be realized by replacing the thick Nb wire with thin NbN. Near T_c , NbN has a thermal conductivity of 1 -- 2 orders of magnitude lower than Nb [23]. Increasing transition temperature (or reducing the operation temperature) further decreases the electronic contribution to G , which scales as $\propto \exp(-T/T_c)$ at $T \ll T_c$. $G = 10 \text{ nW/K}$ would provide operating close to the phonon noise limit with $\text{NEP} \approx 10^{-14} \text{ W/Hz}^{1/2}$ at 4 K and further reduction of the amplifier contribution (Fig. 4a) allowing multiplexing of larger arrays. This figure is comparable to the electrical NEP of the detectors used in a functional video-rate THz scanner [1]. We also note that in order to operate entirely in the phonon noise limit with $\text{NEP} \leq 3 \times 10^{-15} \text{ W/Hz}^{1/2}$, junctions with lower R_T (higher dI/dT) have to be used. However, this will also reduce R_d resulting in a larger mismatch. As a compromise we give the calculated NEP in Fig. (4b) for a detector with $R_T = 20 \Omega$ and $R_d = 1 \text{ k}\Omega$ assuming the present preamplifier $R_a = 53 \text{ k}\Omega$. In addition, a noise line for an impedance matched preamplifier with the same noise temperature of 0.4 K is given. Choosing the room temperature preamplifier with lower R_a becomes a trade-off between keeping both sufficient noise matching and low enough $T_{n,a}$. Another alternative is to use cryogenic amplifiers [24]. Nevertheless, with multiplexing the NEP requirement is less restrictive since all the pixels are averaged continuously, thus in terms

of electrical NEP our experimental results (Fig. 4a) can be sufficient.

5.2. Multiplexing Scheme

Next, we analyze the feasibility of time domain multiplexing based on a cryogenic MUX switch and on a semiconducting amplifier with the feedback circuit as in our experiment (Fig. 5). A cold CMOS MUX-switch can be fabricated with adequate noise parameters and heat dissipation as low as $\approx 1 \mu\text{W/channel}$ [25].

Assuming frame period T_0 and readout bolometer time Δt , the maximum number of multiplexed detectors is $N = T_0 / \Delta t$. A capacitor C is added in parallel to the detector to integrate the signal electrically and to maintain the operating point when the amplifier is not connected. The value of C is chosen in such a way that during the off-time of the readout the detector voltage drop $\Delta V \sim IT_0 / C$ is much smaller than the operating voltage V .

The requirement on C can be relieved by introducing an additional bias circuit for compensating the leakage current as shown in Fig. (5). In this scheme the detector is voltage biased during the time of the corresponding switch being closed but current biased otherwise. Even in the case of full dark-current compensation we still need to integrate the detector noise. This is required if the thermal time constant of the detectors cannot be increased due to practical reasons or if shot noise is comparable to phonon noise and thus needs to be integrated. With dark-current compensation the capacitance value needed becomes

$$C \geq T_0 / R_d \quad (2)$$

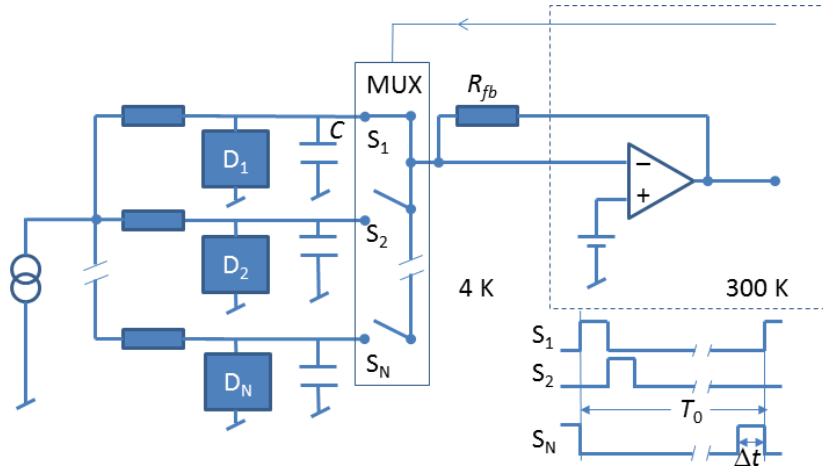


Fig. (5). A Possible multiplexing scheme. The bolometers are voltage-biased during read-out time Δt and current-biased in the off-state to compensate for the leakage current. The bias resistors larger than R_d provide uniform bias of the bolometers.

This allows to use lower C which is favorable for a circuit design.

5.3. Multiplexing Penalty Due to Readout Noise

The capacitance C provides the additional complication that it makes the impedance seen by the readout amplifier frequency dependent. In the following we derive an expression that takes into account the effect of such frequency dependent source impedance on the power resolution.

We describe the signal as the current integrated over the scanning period, i.e. as charge

$$Q_d = (dI/dT)PT_0/G,$$

where P is the detected signal power. We calculate the corresponding charge noise $\sigma_{A,Q}$ due to the voltage noise of the preamplifier in terms of Allan variance (see e.g. [26]).

The calculation is given in the appendix. Equating Q_d with the Allan deviation, we get the power resolution due to a voltage noise limited amplifier

$$\sigma_{A,P} = \frac{G\sigma_{A,Q}}{(dI/dT)T_0} = \frac{GC\sqrt{2S_{V,a}f_{\max}}}{T_0(dI/dT)}. \quad (3)$$

Here f_{\max} is the cutoff frequency of the electronics. Although the noise spectrum is not white, we give an effective power spectral density figure with respect to the full frame time T_0 to help to compare with the detector noise of Eq. ((1))

$$S_p = 2T_0\sigma_{A,P}^2 = \left(\frac{2GC}{dI/dT}\right)^2 \frac{S_{V,a}f_{\max}}{T_0}. \quad (4)$$

Current noise can usually be neglected in the case of CMOS or JFET amplifiers and in any case its treatment is trivial.

Defining parameters $\alpha = R_d C / T_0 \geq 1$ and $\beta = f_{\max} \Delta t = f_{\max} T_0 / N > 1$, we get

$$\sigma_{A,P} = \sqrt{\frac{2S_{V,a}\beta N}{T_0}} \frac{\alpha G}{R_d dI/dT}. \quad (5)$$

Capacitive biasing adds thus a factor $\alpha\sqrt{\beta} > 1$ to the standard multiplexing penalty \sqrt{N} of time-division multiplexing.

As an example, let $N=100$ pixels and $T_0=30$ ms for video rate. With an improved detector design and high quality junctions it is possible to get $R_d \approx 10$ k Ω , $dI/dT = 3.5$ μ A/K, $G = 10$ nW/K, $\sqrt{S_{V,a}} = 0.8$ nV/ $\sqrt{\text{Hz}}$, $\alpha = 1$ and $\beta = 3$. Eq. ((5)) then gives

$$\text{NEP} \approx 30 \text{ fW @ 30ms}$$

with corresponding effective PSD

$$\sqrt{S_p} \approx 8 \text{ fW} / \sqrt{\text{Hz}}.$$

These figures are well sufficient for wideband passive THz imaging [1].

SUMMARY

We have demonstrated suspended SIS tunnel junction bolometers operating in a 4 K temperature regime. Our first prototype devices perform in a shot noise limit. With introducing thinner superconducting materials together with low resistance junctions and proper optical coupling the NETD of the bolometers performance is expected to be in the ballpark of ~ 1 -10 mK. Furthermore, the fact that the noise in the SIS-bolometer device is strongly dominated by the detector itself rather than by the read-out setup, suggests that they are suitable for multiplexing. The possibilities of a wide-band THz signal matching and good amplifier matching make SIS-bolometer a promising candidate for large array imagers.

ACKNOWLEDGEMENT

We thank J. Penttilä, M. Grönholm, H. Sipola, A. Rautiainen, M. Kiviranta and M. Leivo for useful

discussions and help with the experimental setup. This work is financially supported by the Academy of Finland Grant No. 140484 (Cryogenic Microsensors), and Finnish Academy Program for Center of Excellence in Low Temperatures and Quantum Devices.

CONFLICT OF INTEREST

The authors confirm that this article content has no conflicts of interest.

APPENDIX

Charge Fluctuations Due to Voltage Noise on a Capacitive Load

The source impedance seen by the preamplifier is frequency dependent, which complicates the noise analysis. We give here a calculation based on charge balance and Allan variance between successive measurements. With Allan variance temporal variations of a stochastic process can be linked to its power spectral density. This analysis is applicable to other time domain multiplexing cases with frequency dependent source impedance. In our case the source impedance is dominated by the capacitance C

$$|Z_{\omega}| = 1/\omega C.$$

During time T_0 , the voltage over the detector changes by ΔV

$$Q_d = C\Delta V \approx IT_0,$$

where I is average signal current and uncompensated leakage current through the bolometer. This is compensated by a loading current I_a

$$Q_l = Q_d = I_a \Delta t$$

during the charging/readout period Δt .

The charge noise driven into the capacitor during loading time is the noise current $i(t)$ caused by the voltage noise of the readout, integrated over time Δt

$$q_l = \int_0^{\Delta t} i(t) dt.$$

Let us compare this to a neighboring measurement and calculate the Allan variance [26]. We get

$$\begin{aligned} \sigma_{A,Q}^2 &= \left\langle (q_{j+1} - q_j)^2 \right\rangle / 2 = \left\langle q_j^2 - q_j q_{j+1} \right\rangle \\ &= \iint i(t) i(t') dt dt' - \iint i(t) i(T_0 + t') dt dt' \\ &= \int_0^{\infty} \frac{df}{(\pi f)^2} S_{I_V}(f) \sin^2(\pi f \Delta t) (1 - \cos(2\pi f T_0)), \end{aligned}$$

where time integration intervals are from 0 to Δt . Here

$$S_{I_V} = S_{V,a} / |Z_s|^2 = (2\pi)^2 S_{V,a} C^2 f^2$$

is the power spectral density of the current noise caused by the voltage noise $S_{V,a}$. The result diverges for a noise source with infinite bandwidth. In practice a high frequency cutoff

f_{\max} always exists. To avoid signal crosstalk between detectors, $f_{\max} > 1/\Delta t$. Assuming $\Delta t < T_0$, we end up at

$$\sigma_{A,Q}^2 \approx 2S_{V,a} C^2 f_{\max}. \quad (A-1)$$

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Received: March 28, 2012

Revised: May 14, 2012

Accepted: May 29, 2012

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