

Data Preprocessing Circuit Designs and Analyses for Subsonic Cruise Missile Infrared Image Seeker

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Abstract: In this literature, a data pre-processing circuit designs and analyses for an infrared image seeker of a subsonic cruise missile are proposed. The flight speed of the missile is 250m/s. Therefore, automatic contrast control and automatic brightness control are usually needed to keep wanted contrast and brightness for best image properties in target recognition and tracking; especially for the missile at the terminal approaching phase to prevent signal saturation. Signal saturation implies target lost. The major parameters of imaging systems for contrast and brightness controls include average, maximum and minimum of gray level of the whole picture in the sampling interval. Therefore, the overall system is a complicated nonlinear sampled-data control system. It is difficult to describe, analyze and design the system. In this literature, the overall system is first decomposed into two linearized sampled-data control systems to get needed loop compensations, and verified by a special range-dependent testing signal to the overall system secondly. The controlled system is further verified by a thermal plate and real flight testing. Testing results give the proposed method can provide effective way to analyze and design the considered system.

INTRODUCTIONS

Multiple sensors for ECCM are generally expected for missile homing systems. The first dual seeker application of the world for subsonic antiship missile is the HF-2 developed in Taiwan. It includes a Radar seeker and an IR image seeker with data fusing algorithm. This application prompts the capability for ECCM from chaff jamming to RF seeker and thermal jamming to IR Image seeker. The problem for IR image seeker for the missile application is the thermal energy to the charge-coupled device (CCD) [1-3] is the fourth power of the reciprocal the distance between image seeker and thermal source; i.e.; missile and target. It needs faster automatic gain control (AGC) for contrast and automatic pedestal control (APC) for brightness [4-7] to keep best imaging properties for target recognition and tracking; especially for the missile at terminal approaching phase. Slower response for coping with thermal energy increasing will make the signal output be saturated; i.e., all white of full frame and will lost tracking centre point for missile guidance.

In this literature, models for contrast, brightness and dynamic signals are proposed to analyses and designs of the AGC and APC circuit loops. The AGC and APC are the pre-processing systems of the video signal. The block diagram considered is shown in Fig. (1). It includes thermal sensors, analog circuits, and microprocessor. The output of the processed signal satisfies RS-170 video standard. The sensor elements for thermal imaging is 256×256 focal plane CCD[1-3], the gray level of each element is digitalized to be 256 states with 8bit A/D converter. Fig. (2) shows typical minimal and maximal gray level of a thermal picture. Analog circuit gives faster signal amplification in forward-loop. It needs $256 \times 256 \times 60 \times 10$ Hz at least for 60Hz picture

updating frequency ; i.e., greater than 39.32MHz. Micro-processor providing thermal cell addressing for sampling and storing CCD sensing data, maximal, minimal, and average values calculations of the whole picture and digital filters for AGC and APC loops.

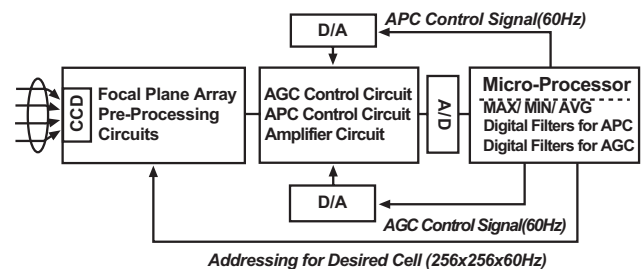


Fig. (1a). Block diagram of the data pre-processing.

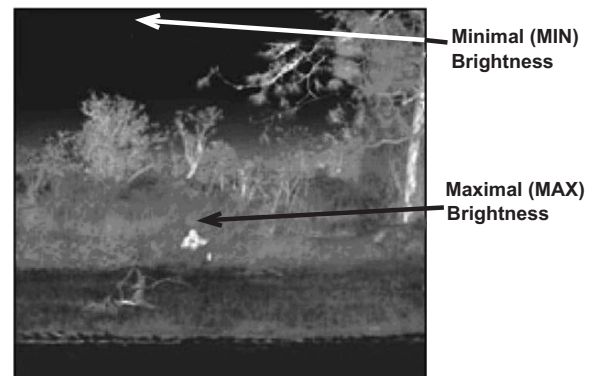


Fig. (1b). The minimal and maximal Brightness of a thermal picture.

Fig. (2) shows the analog circuit component of the overall system. The input is "Signal" form CCD and output is digitalized with AD converter ADC208, the "Gain" is the connection for AGC with 60Hz updating rate. The "Offset" is the connection for APC with 60Hz updating rate, the "Extern" is the selection for APC/AGC and manual adjustment

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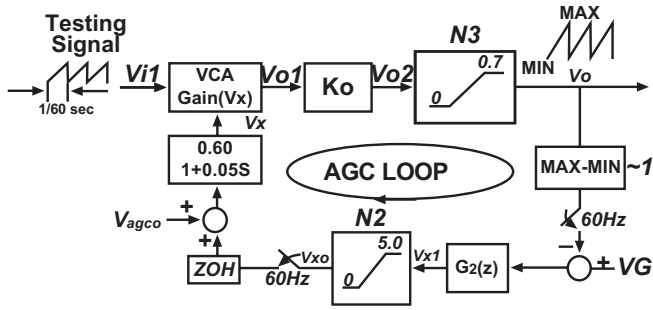


Fig. (4). The AGC loop and linearization for MAX-MIN.

The input/output relationship of each block in steady-state condition given in Fig. (4) can be written by following equations:

$$V_{o2} = K_o V_{i1} V_x \quad (2)$$

$$V_o = N_3(V_{o2}) \quad (3)$$

$$V_{x1} = G_2(z) \Big|_{z=1} (V_G - V_o) \quad (4)$$

$$V_{xo} = N_2(V_{x1}) \quad (5)$$

$$V_x = 0.6(V_{xo} + V_{agco}) \quad (6)$$

Note that the value V_{i1} is the maximal value of the input signal MAX in the sampling interval. From Equations (2) to (6), one has

$$V_x = 0.6\{N_2(G_2(z) \Big|_{z=1} [V_G - N_3(K_o V_{i1} V_x)] + V_{agco}\} \quad (7)$$

The steady-state solution V_{xs} (and V_{os}) can be found by Equation(7) for specified values of input signal V_{i1} and command V_G . Since the values of V_x is limited in $[0.6 V_{agco}, 3V]$, the found solution V_{xs} in $[0.6 V_{agco}, 3V]$ represents the AGC loop is active for this specified value of V_{i1} . If there is no solution found, the value of V_{xs} will be at $0.6 V_{agco}$ for large value of V_{i1} or at $3V$ for small value of V_{i1} . Note that steady-state conditions exist only for stable system. Therefore, the $G_2(z)$ must be designed not only for performance but also for stability.

Fig. (5) shows solutions of solving Equation (7) for $V_G=0.5V$, $K_o=12$, $G_2(1)=100$, $V_{agco}=0.05V$, and some specified values of V_{i1} . It represents the AGC loop is active for magnitudes of input signal V_{i1} between A and B. Since the saturating level of N_3 is $0.7V$, magnitudes of input signal between B and C are still useful. Beyond C, signals will be distorted by introducing the saturating nonlinearity N_3 . Fig. (5) represents the operating range of input signal of the AGC loop; i.e., the maximal acceptable and the minimal detectable signal inputs. Base upon the operating range found, one could determine the gains of transducer K_i (e.g., gains of thermal-electrical device [1-3]) for a given maximal possible relative temperature difference of the target. $25mV/^\circ C$ is selected. On the other hand, one can adjust values of K_o and V_{agco} to get the suitable operating range for a given gain of transducer. The value of V_{agco} determines the maximal acceptable signal input of the AGC loop (Point B given in Fig. 5), and the feed-forward loop gain K_o determines the minimal detectable signal input (Point A given in Fig. 5).

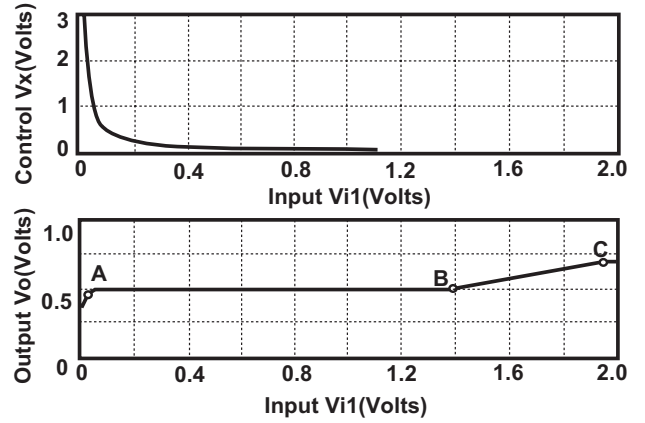


Fig. (5). Output and control gain versus input signal.

The Voltage Control Amplifier VCA can be replaced by $V_{i1} V_{xs}$, in which V_{xs} represents the steady-state solution of the AGC loop found by solving Equation (7) for a specified value of V_{i1} . The transfer function of form V_G to V_o can be written as

$$V_o(z) = \frac{K_o G_2(z) Z \left\{ \frac{0.6}{1 + 0.01s} \right\} V_{i1s} V_{xs}}{1 + K_o G_2(z) Z \left\{ \frac{0.6}{1 + 0.01s} \right\} V_{i1s} V_{xs}} V_G(z) \quad (8)$$

where $Z\{\cdot\}$ is operator of the z-transformation. Equation (8) gives if the AGC loop was in active region(between point A and point B shown in Fig. 5); frequency responses given by Equation(8) will keep the same even for different input values of V_{i1} . Therefore, the output V_o will track command V_G . Equation (8) can be used also to find digital controller $G_2(z)$ for wanted performance and robustness. The found digital controller [10, 11] for phase margins greater than 60° and gain margin greater than 6dBs is in the form of

$$G_2(z) = 100 \frac{1 + 0.02s}{1 + 2.00s} \Big|_{s = \frac{2(Z-1)}{T_s(Z+1)}} = \frac{z - 0.1700}{z - 0.9917} \quad (9)$$

where T_s is equal to 1/60 seconds [4]. T_s is called frame time also. Fig. (6) shows open-loop Bode diagrams with values of input signal V_{i1} varying from 0.1V to 1.3V. It gives phase margin 67.5° and gain margin 26dBs. The frequency responses show the compensated system is robust against the variation of input signal V_{i1} in the active range. The corresponding steady state values of V_x and V_o are given in Table 1.

The bandwidth of the AGC loop is about 3.0Hz. The suitability of 3.0Hz will be verified by time responses with input signal increasing. The phase margin $PM=67.5^\circ$ is equivalent to pure time delay ($T_d = 91.85ms$) in loop, and gain crossover frequency ($f_n=2.04Hz$) are all given in Fig. (6). The phase margin will be verified by digital simulations with equivalent pure time delay (T_d); i.e., V_{xo} is replaced by $V_{xo} e^{-T_d s}$.

Fig. (7) shows time responses of the AGC loop with digital controller $G_2(z)$ for $V_G=0.5V$. The input testing signal is

Table 1. Calculated and Simulated Results for Different Inputs

Inputs	Calculated Results		Simulated Results	
V_{il}	V_{xs}	V_{os}	V_x	V_o
0.01	3.00	0.360	3.00	0.398
0.05	0.811	0.487	0.740	0.489
0.10	0.411	0.493	0.376	0.494
0.30	0.138	0.498	0.126	0.500
0.50	0.083	0.500	0.075	0.500
0.75	0.056	0.500	0.054	0.500
1.00	0.042	0.500	0.038	0.500
1.25	0.033	0.500	0.030	0.503
1.50	0.030	0.540	0.030	0.569
2.00	0.030	0.700	0.030	0.700

a saw-tooth wave shown in Fig. (4) and maximums in sampling intervals are increasing with a time function in the form of

$$F(t) = \left(\frac{\gamma}{\beta - t} \right)^2; \quad (10)$$

where t is the simulation time, $\beta=5.238$, and $\gamma=0.3704$. The value 5.238 represents 1.3Km for missile speed equal to 250m/s. The control voltage v_x of VCA is decreasing for the input signal v_{il} is increasing with the time function described by Equation (10), and the output v_o keeps almost constant (~0.5V) in the active range. It can be seen that v_x keeps its maximal value (3V) in the inactive range (before 2 seconds), and 0.6 v_{agco} after 4.8 seconds. Fig. (7) shows the closed-loop system compensated by $G_2(z)$ is suitable for given target approaching behaviors. A stop homing range ~250m is usually selected for RF seeker used only. It prevents the saturation of the RF receiver. The less stop homing range, the less miss distance will be. Fig. (7) shows that the shorter stop homing range can be selected for less miss distance.

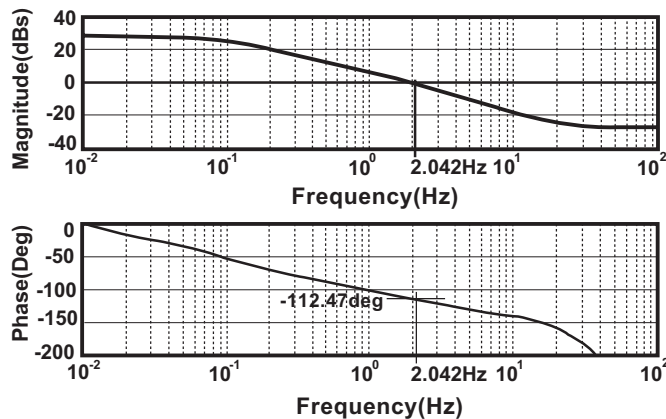


Fig. (6). Open-loop Bode diagram of AGC loop($V_{il} = 0.1\sim 1.3$).

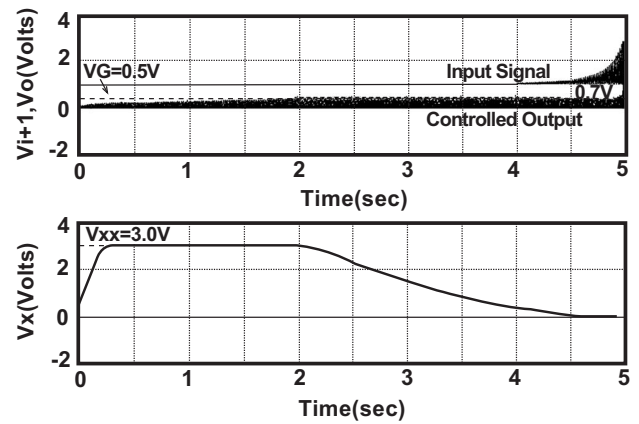


Fig. (7). Time responses for subsonic approaching.

Note that the feedback signal of the AGC loop is MAX-MIN, thus saw-tooth wave can be used to simulate maximums and minimums of real input signals in sampling intervals for there is no frequency element in the feed-forward signal loop(i.e., from v_{il} to v_o). Fig. (7) shows active, inactive, and saturating characteristics of the AGC loop (c.f. Fig. 5). Several constant-input v_{il} , digital simulations are made also to verify calculated results from the simplified AGC loop. The simulated and calculated results are given in Table 1. It can be seen that calculated results give good agreements with simulated results. Note the magnitude of V_{il} , stated in steady-state analyses is equivalent to maximal amplitude of the saw-tooth wave in sampling interval in digital simulations.

The stability [12,13] of the AGC loop compensated by Equation (9) can be verified by introducing the maximal acceptable pure time delay(T_d) into loop in digital simulation. Note that the maximal acceptable pure time delay represents the system will become unstable by introducing pure time delay greater than T_d . Fig. (8) shows the stability testing for the maximum of input signal v_{il} equal to 0.05V with 6/60 seconds pure time delay into the position between the digital control signal v_G -MAX+MIN and $G_2(z)$. The oscillating frequency of the control signal v_x is 1.85Hz, which is close to the found gain crossover frequency given in Fig. (6) with the value of MAX-MIN is set to be unity. This implies that the simplification is satisfactory. The relationship between the maximal acceptable pure time delay (T_d) in second, phase margin (PM) in degree, and oscillating frequency (f_n) in Hertz is in the form of

$$PM = 360 f_n T_d \text{ deg.} \quad (11)$$

Based upon Equation (11), the found phase margin is 65.5° in the above constant- v_{il} stability testing. It is closed to that of the analyzed result shown in Fig. (6).

Fig. (9) shows time responses with $T_d=6/60$ seconds pure time delay in loop, and magnitudes of input signal v_{il} are increasing with the time function described by Equation(10). It can be seen that the phase margin of the AGC loop is greater than 60° for the maximal acceptable loop pure time delay $T_d =6/60$ seconds and the oscillating frequency $f_n = 2.0$ Hz. The results shown by Figs. (8) and (9) represent dy-

namics of the compensated system are close to results of analyses with the simplification of the AGC loop. Note that it is need not find the gain margin of the AGC loop for its object is to adjust loop gain.

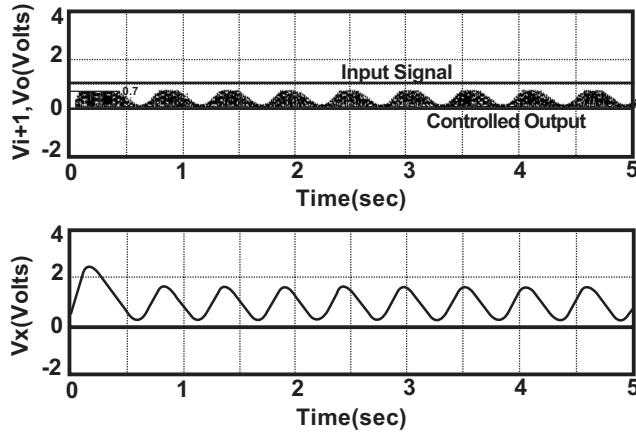


Fig. (8). Simulating verification for AGC loop (1.85Hz) for $V_{i1}=0.05V$ and $T_d=100ms$.

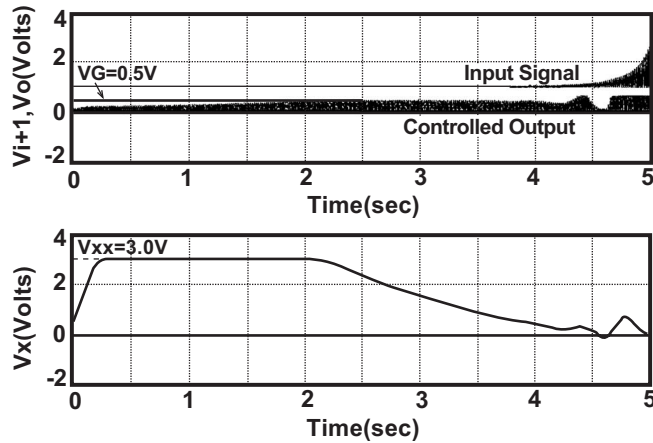


Fig. (9). Simulating verification for AGC loop for varying input and $T_d=100ms$.

ANALYSES AND DESIGNS OF THE APC LOOP

Fig. (10) shows the APC loop, in which $G_1(z)$ and $1/V_x$ are digital controllers; v_{y2} represents the disturbance of the APC loop from input signals; AVG is set to be unity also; voltage control amplifier(VCA) is replaced by its control voltage v_x . The use of $1/V_x$ is to keep constant loop gain and linearize the APC loop. The values of v_x is determined by the AGC loop and input signal v_{i1} . The found digital controller of the APC loop is given below:

$$G_1(z) = \frac{T_s(z + 1.000)}{2(z - 0.997)} \cdot 1.25 \frac{1 + 0.05S}{1 + 0.005S} \Big|_{s = \frac{2(z-1)}{T_s(z+1)}} \quad (12)$$

The bandwidth of the APC loop is about 2.4Hz. The gain/ phase margins and phase/ gain crossover frequencies are $PM=81.1^\circ$ with gain crossover frequency $f_n = 1.9Hz$ and $GM=10.1$ with phase crossover frequency $f_n = 17.45Hz$. The verifications for gain/phase margins will be discussed with

overall system for I/V_x is used in APC loop shown in Fig. (10).

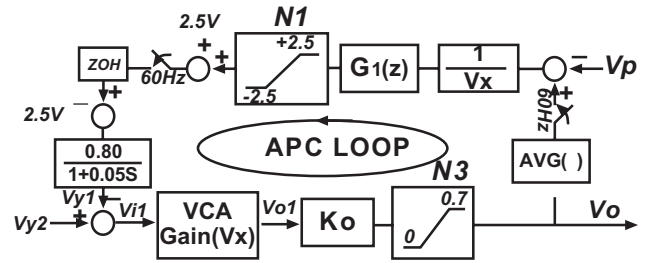


Fig. (10). The APC loop with disturbance V_{y2} .

DIGITAL SIMULATIONS AND VERIFICATIONS OF THE OVERALL SYSTEM

Fig. (11) shows time responses of the overall system for $V_p=0.35V$ and $V_G=0.5V$ with the range-dependent input signal v_{y2} described by Equation(10). It shows operating behaviors of AGC and APC loops. For instance, the output signal v_o before 2 seconds keeps wanted average at $V_p=0.35V$ with maximal control voltage $v_x=3V$. The contrast cannot follow V_G for magnitudes of input signals v_{i1} are too small (c.f., below Point A given in Fig. 5). After 2 seconds, the AGC loop is active, the contrast keep desired value $V_G=0.5V$ until V_x decreased to be $0.6V_{agco}$. Comparing results given in Fig. (7) without the APC, it can be seen that the overall system provides constant brightness with the desired contrast similar to results given in Fig. (7).

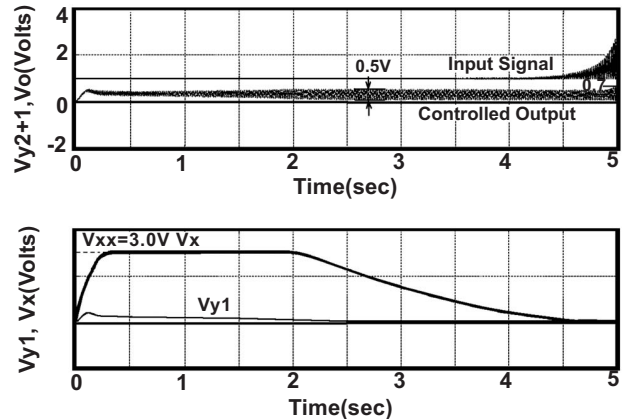


Fig. (11). Time responses for overall AGC/APC loop.

Figs. (12) and (13) show results of stability testing of the APC loop for finding gain and phase margins in digital simulations of the overall system. Fig. (12) shows results of the APC control loop with pure time delay $T_d=7/60$ seconds; Fig. (13) shows results of the APC control loop gain multiplied by 10.1. The equivalent phase margin (PM) can be found by Equation (11). The phase margin is greater than 80deg for the maximal acceptable loop pure time delay $T_d=7/60$ seconds and the oscillating frequency f_n of v_{y2} is about 2Hz. It can be seen that the simplified APC loop shown in Fig. (1) provides a good approximation also for the real system shown by Fig. (3). Therefore, simplifications of two individual loops are satisfactory for analyses and designs of the complicated nonlinear sampled-data control system.

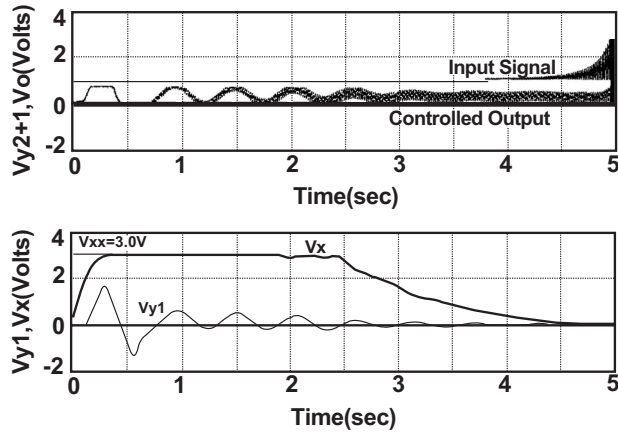


Fig. (12). Time responses of the overall system with $T_d = 7/60$ in APC loop.

Fig. (14) shows the thermal testing plate for target approaching phase. It is made by light emitter diodes (LEDs). The distance between image seeker and thermal plate is 5 meters and dimension of thermal plate is 120cm×88cm for field of view(FOV) of the image seeker is $14^\circ \times 10^\circ$. The dimension of ship-shape is increased with time function described by Equation (10). The testing results gives the function design of AGC/APC circuit is valid for anti-ship applications. Fig. (15) shows the typical thermal image of target on flight testing. The distance between imager and ship is 410m.

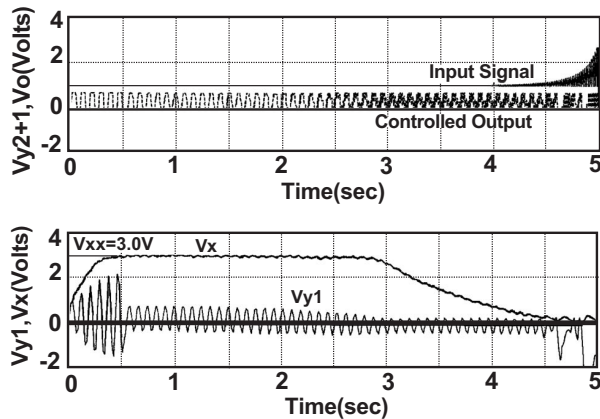


Fig. (13). Time responses of the overall system with Gain=10.1 in the APC loop.

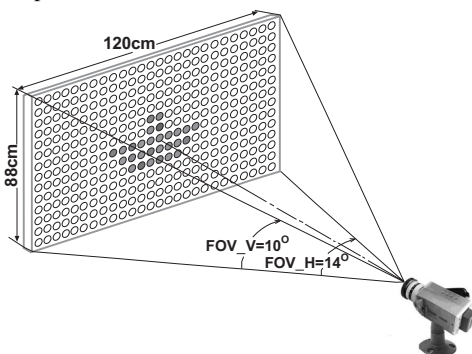


Fig. (14). Thermal plate testing concepts for target approaching phase.

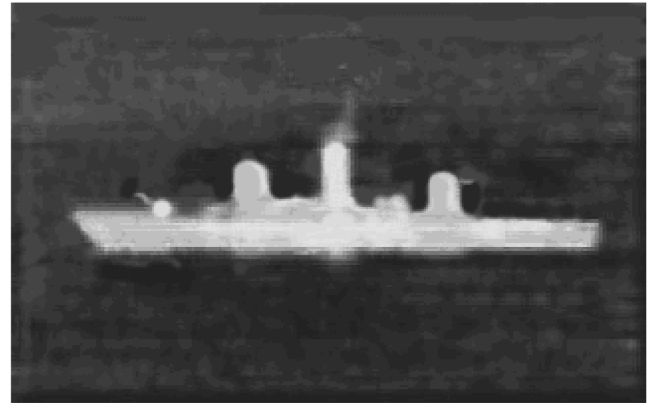


Fig. (15). Thermal image of target in the terminal target approaching phase.

CONCLUSIONS

In this literature, a data pre-processing circuit designs and analyses for an infrared image seeker of a subsonic cruise missile have been proposed. The overall system is a complicated nonlinear sampled-data control system. It was first decomposed into two linearized sampled-data control systems to get desired loop compensations, and verified with a special range-dependent testing signal for the overall system secondly. The controlled system was further verified by a thermal plate and real flight testing. Simulation and testing results gave the proposed method can provide effective way to analyze and design the considered system.

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